IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Kadlecik et al.	Confirmation No.:	6367
Serial No.:	10/015,029	Group Art Unit:	2192
Filed:	12/11/2001	Examiner:	Romano, John J.
Title:	DEBUGGING TRANSACTIONS ACROSS MULTIPLE PROCESSORS		

To: Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

The Examiner's Amendment received with the Allowance papers included an Examiner's Statement of Reasons for Allowance. See page 5 of the Examiner's Amendment. These comments are being filed with the Issue Fee, and are therefore being timely filed.

The Examiner's Statement indicated that allowance was based at least on the aspect of providing debug information "without ... performing debug registration with the another processor." While the Applicants agree that aspect is not disclosed, taught or suggested in the cited art, Applicants submit that the claims are also allowable for their other limitations, and combinations of limitations, for example, those limitations argued by Applicants during prosecution, and limitations presented in the dependent claims.

Respectfully submitted,

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Dated: November 1, 2006.

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